

FIG. 1a
 Shared-Memory Processor Cluster

10a

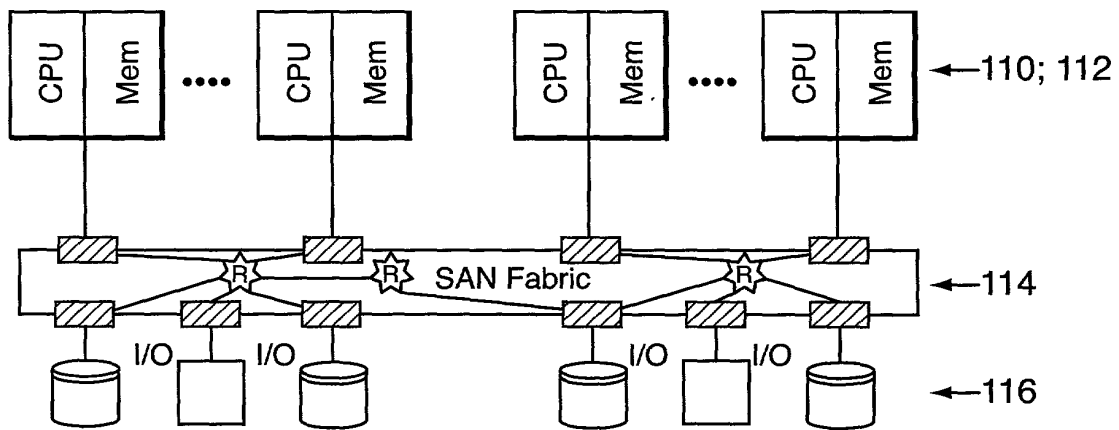


FIG. 1b
 Shared Noting Parallel Processors Cluster

10b

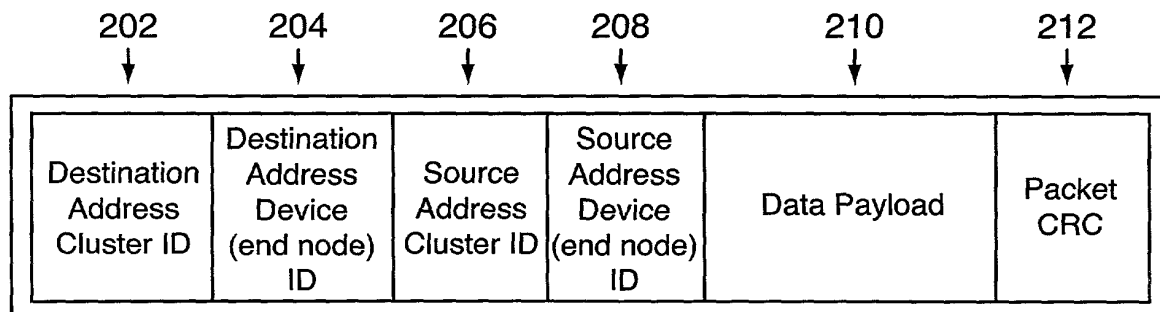


FIG. 18
 Header Format

200

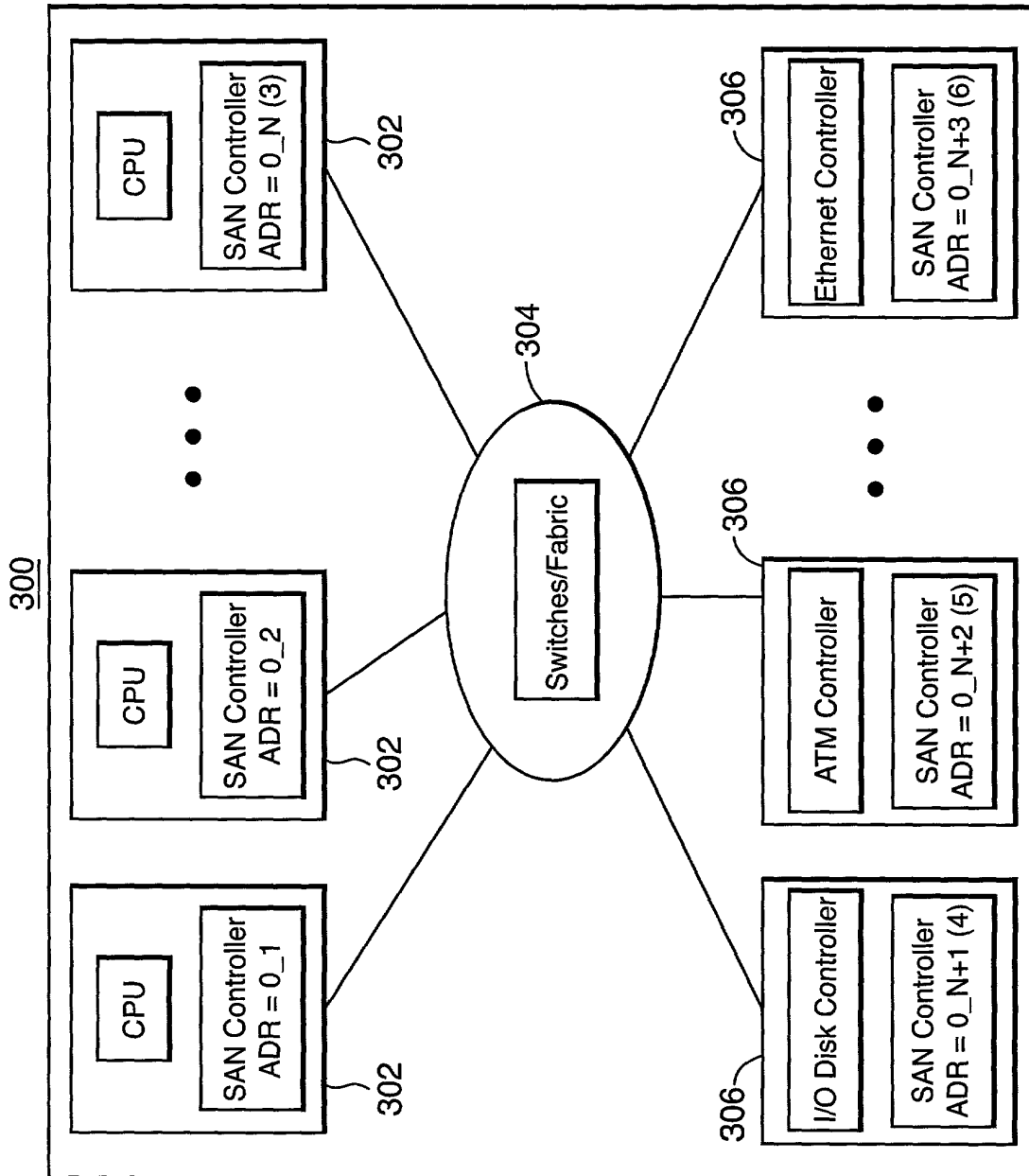


FIG. 2 Cluster Node

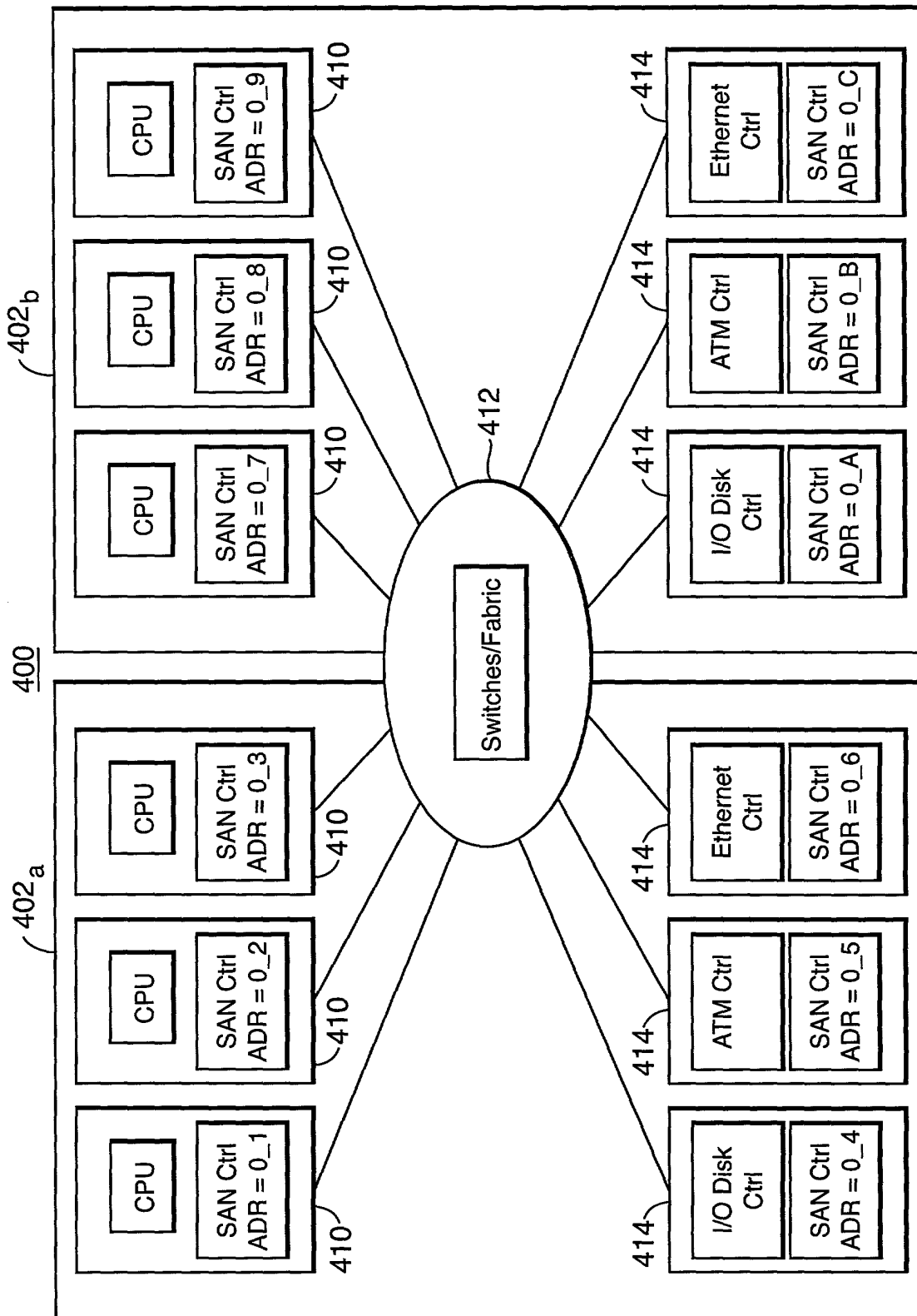


FIG. 3 Flat or Non-Hierarchical Growth

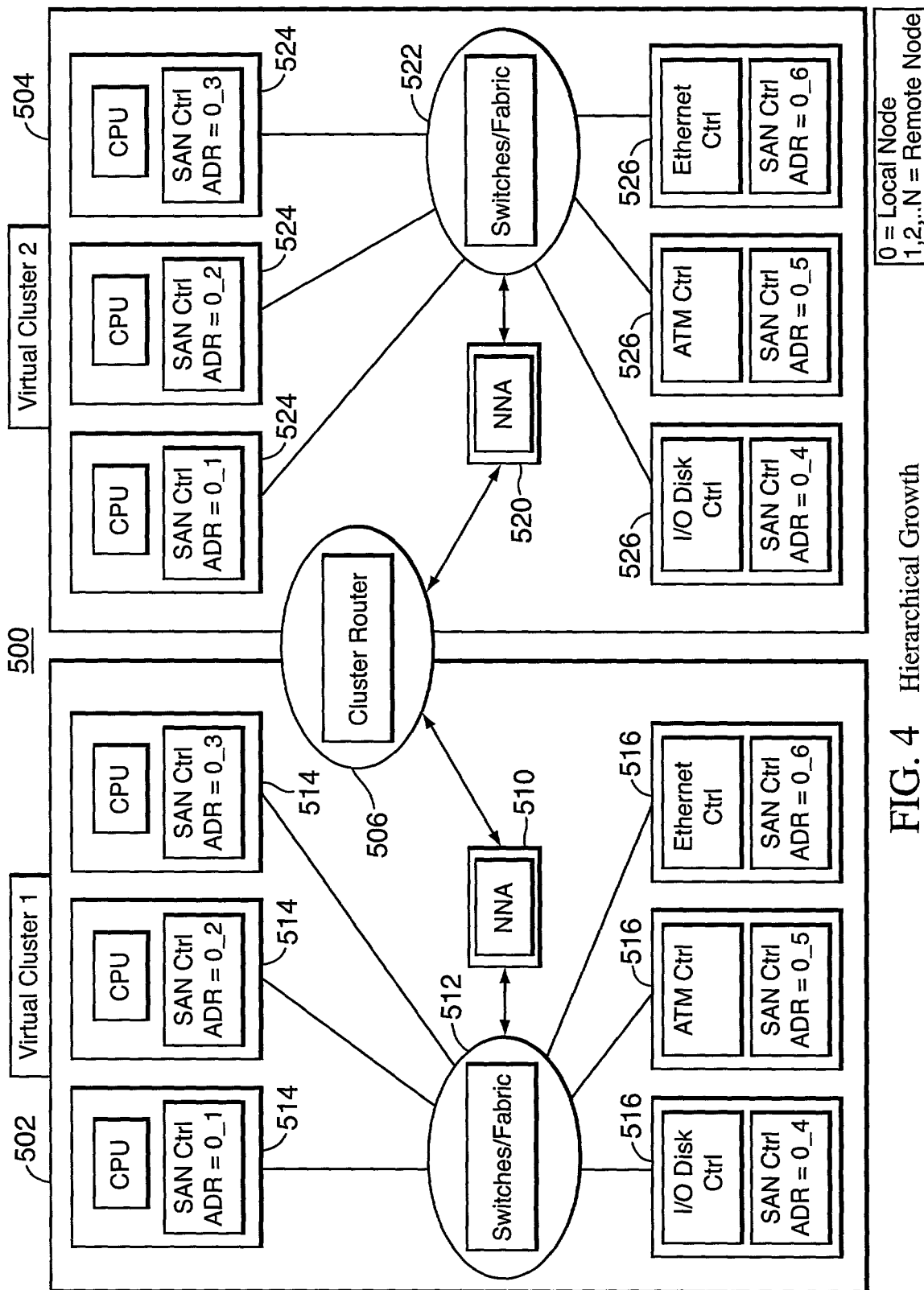


FIG. 4 Hierarchical Growth

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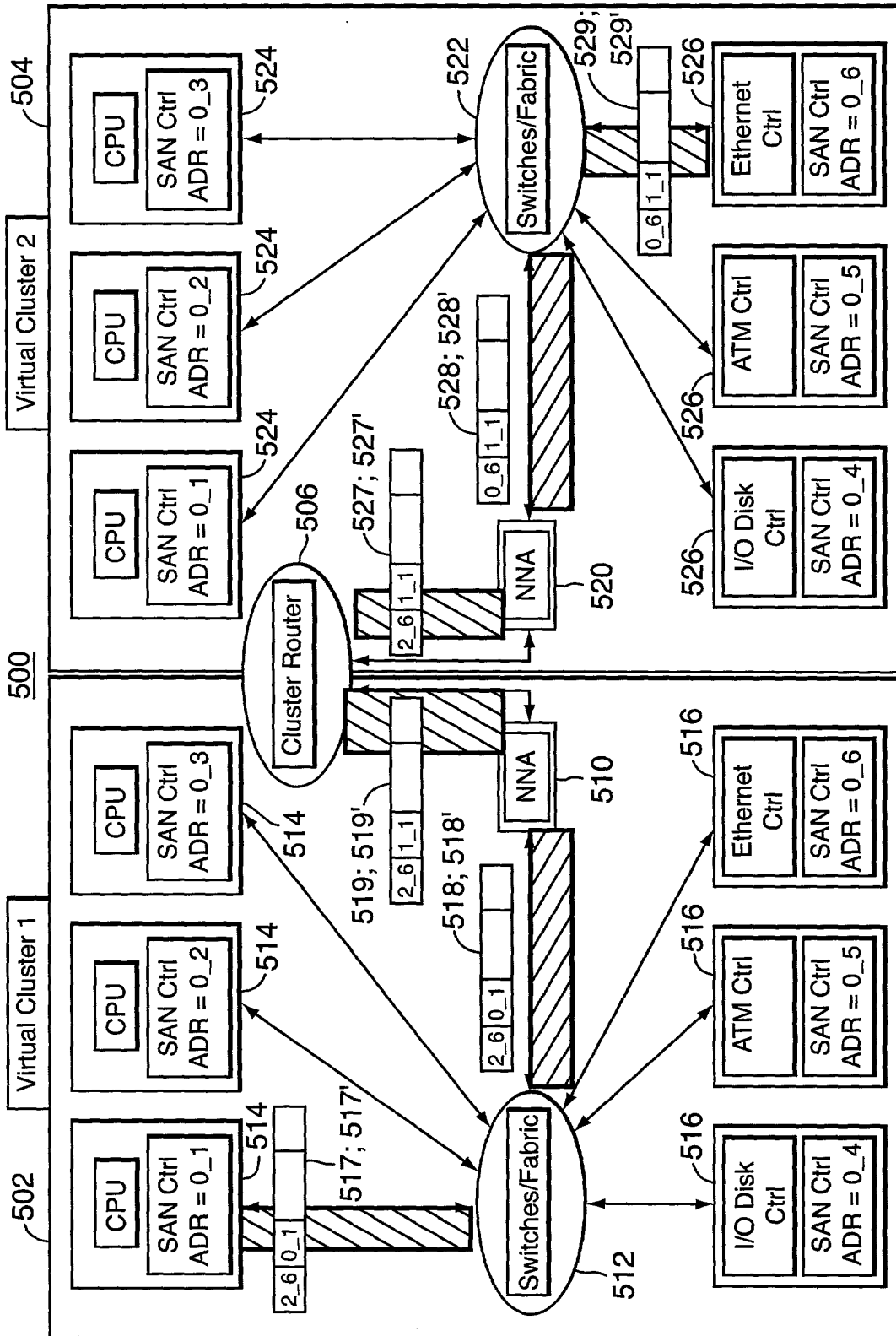


FIG. 5 Full Packet Flow

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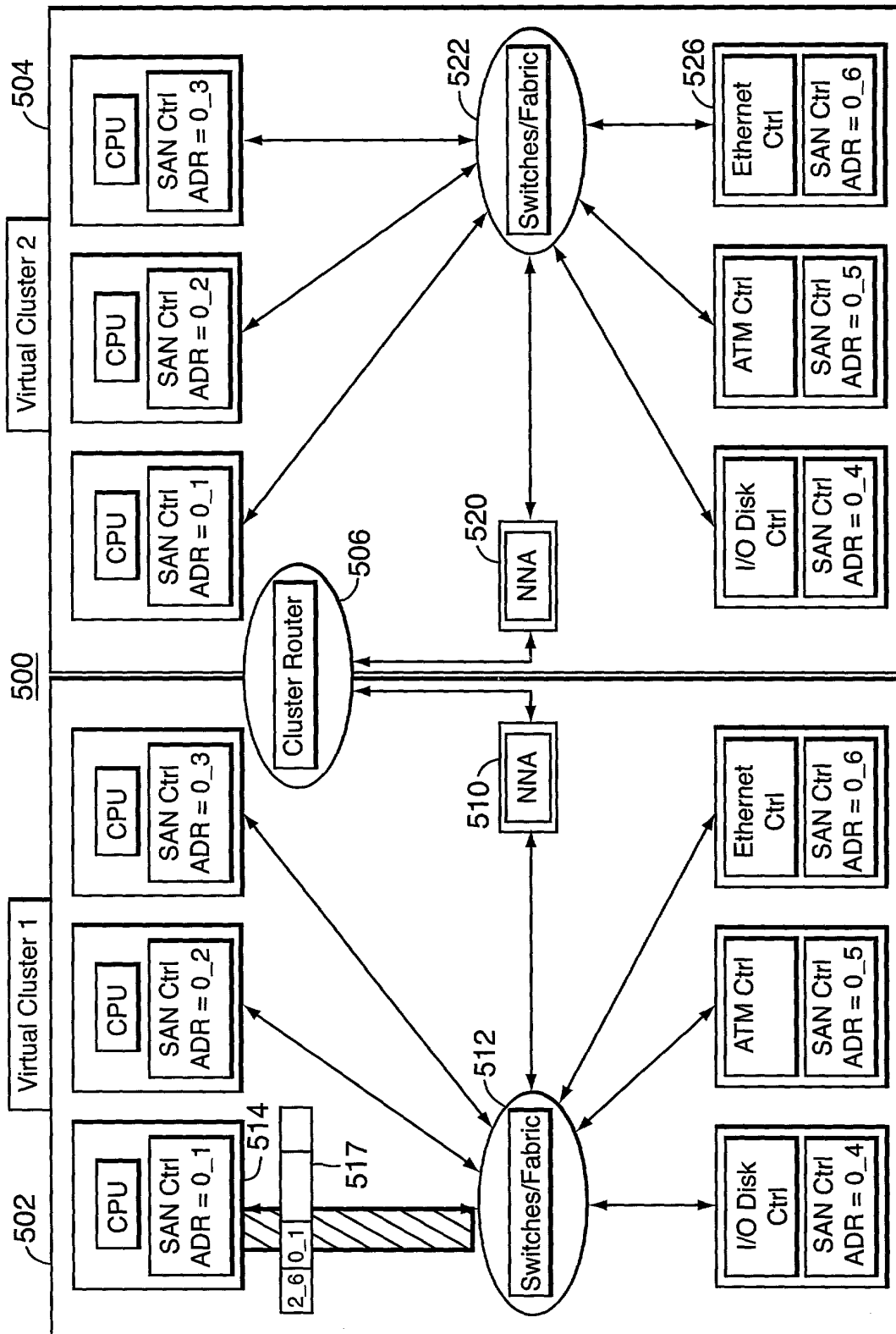


FIG. 6 Packet Flow - Step 1

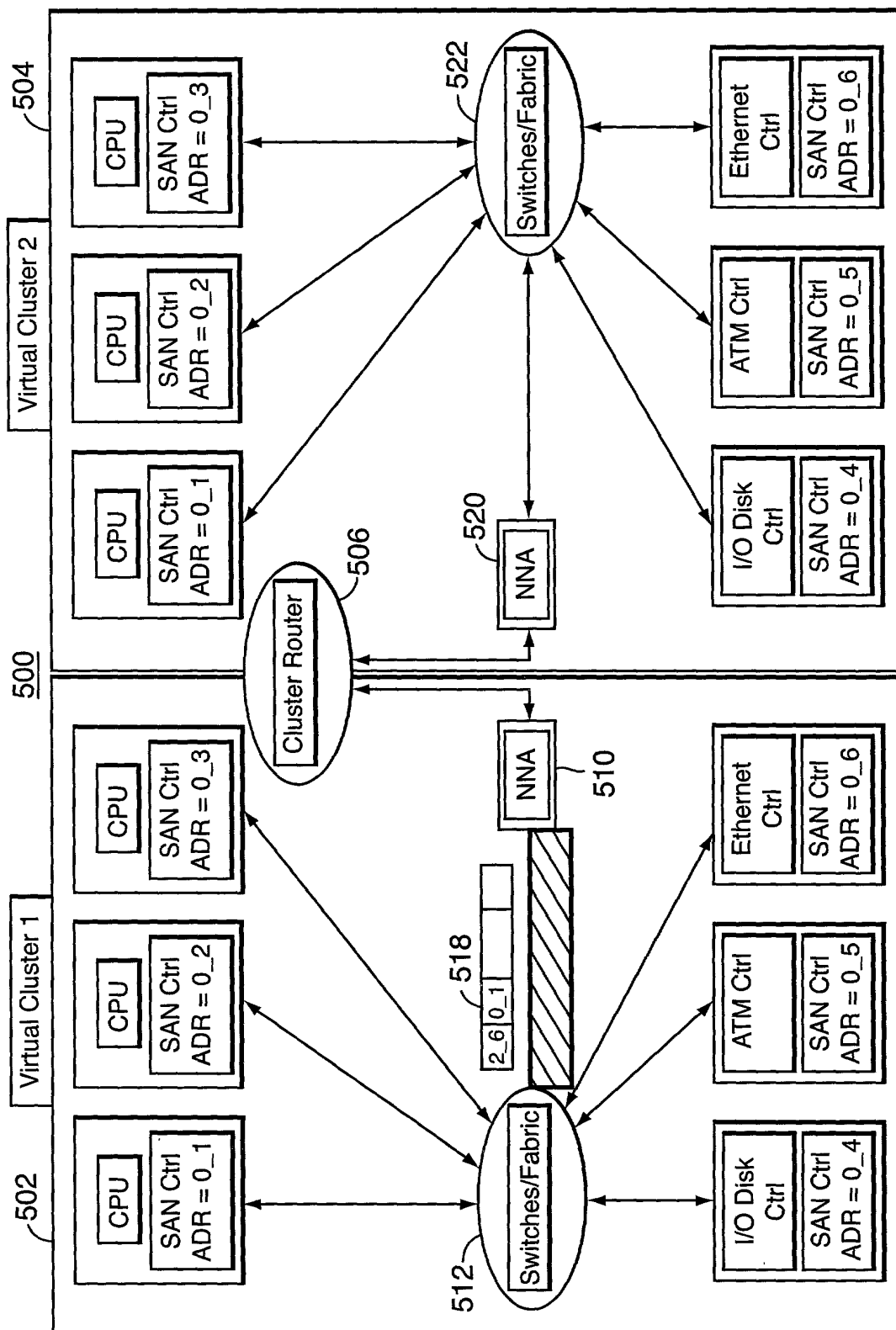


FIG. 7 Packet Flow - Step 2

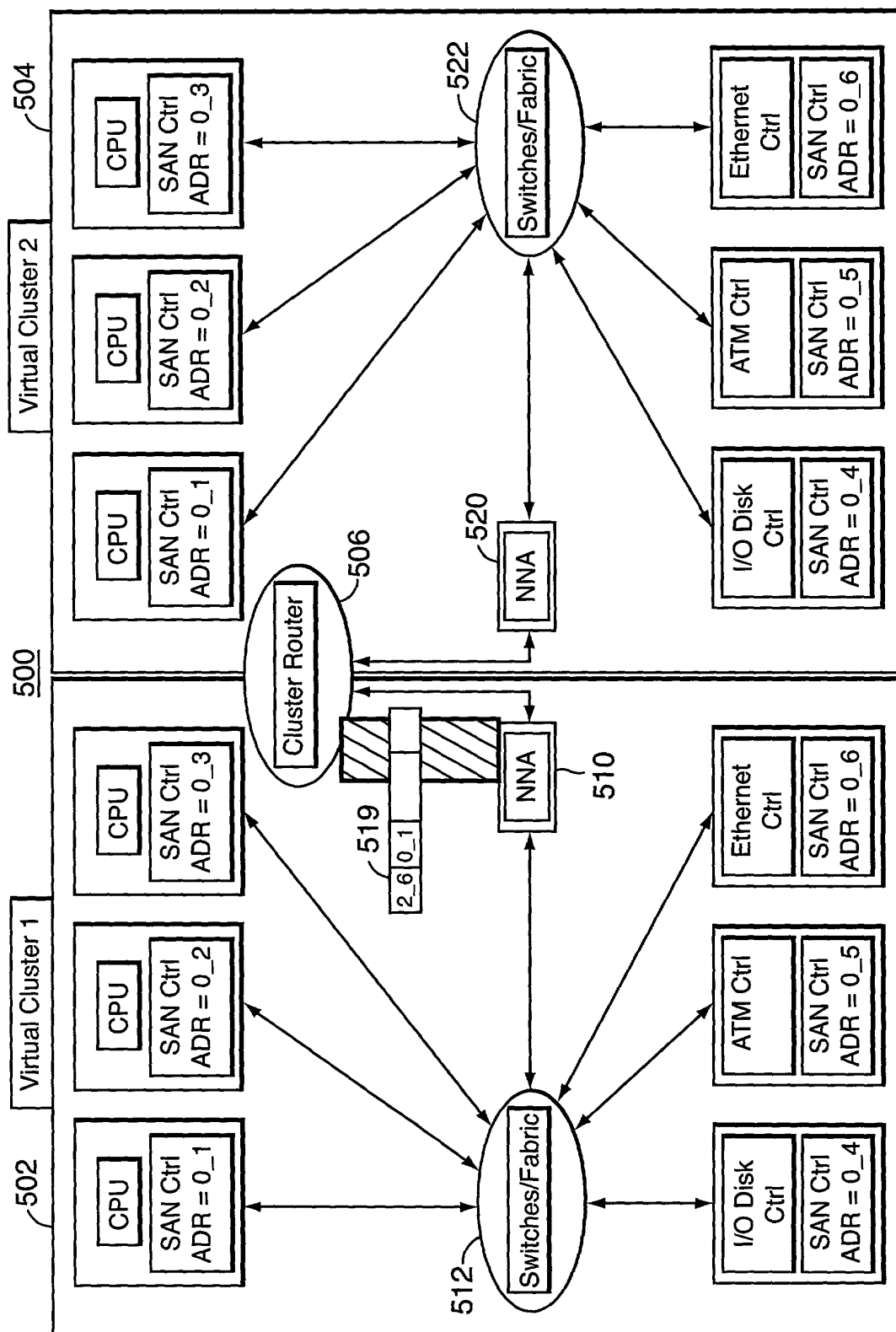


FIG. 8 Packet Flow - Step 3



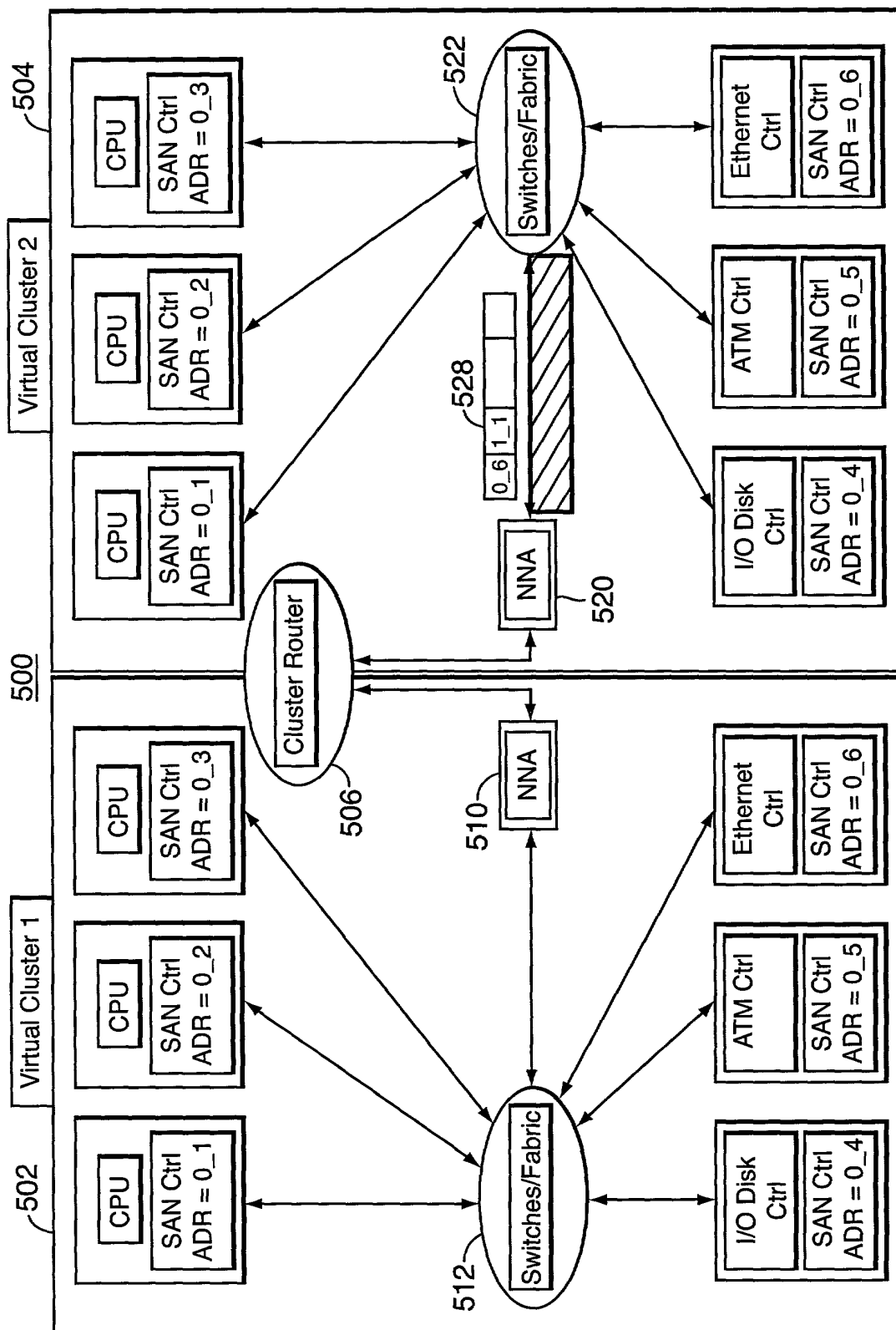


FIG. 10 Packet Flow - Step 5

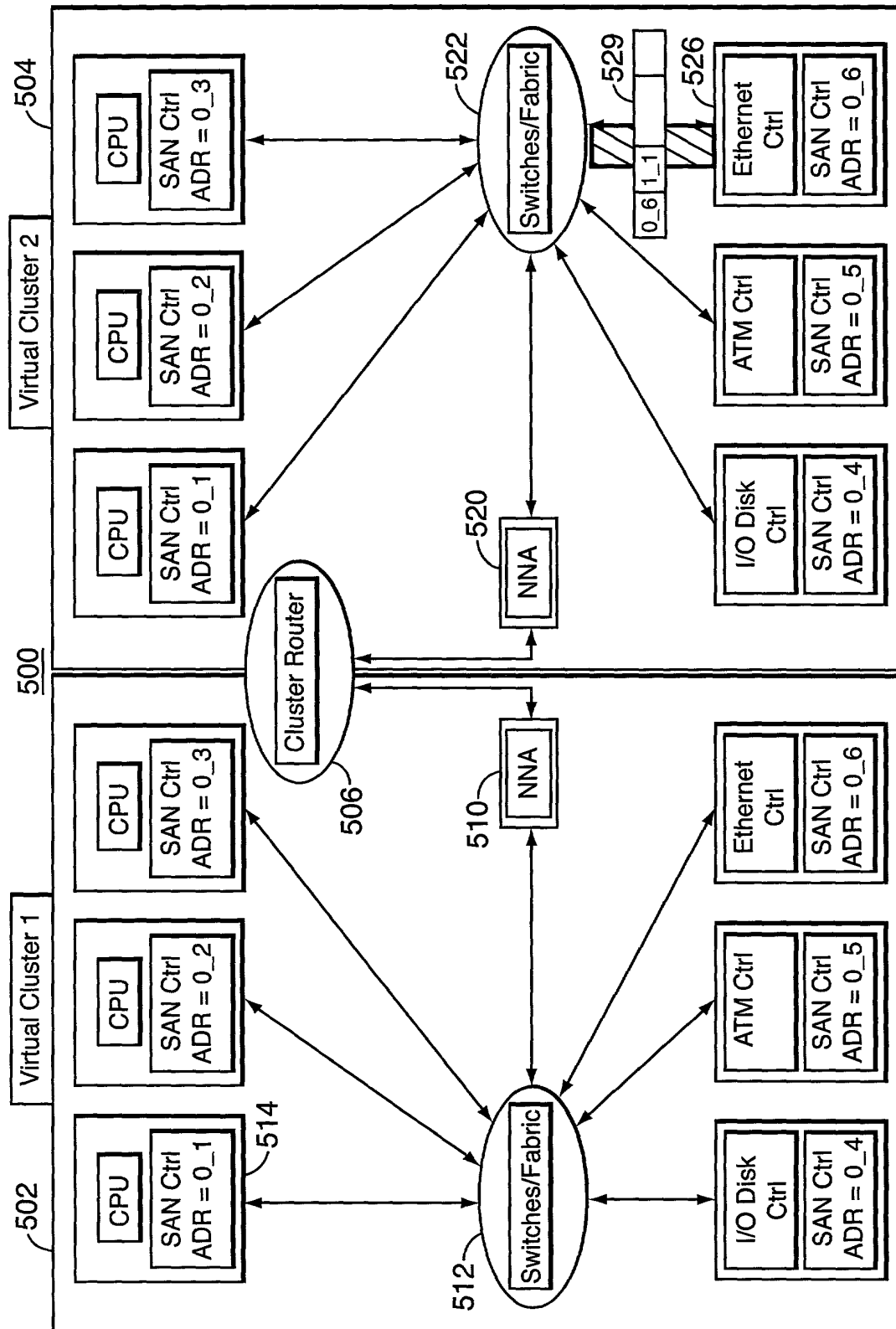


FIG. 11 Packet Flow - Step 6

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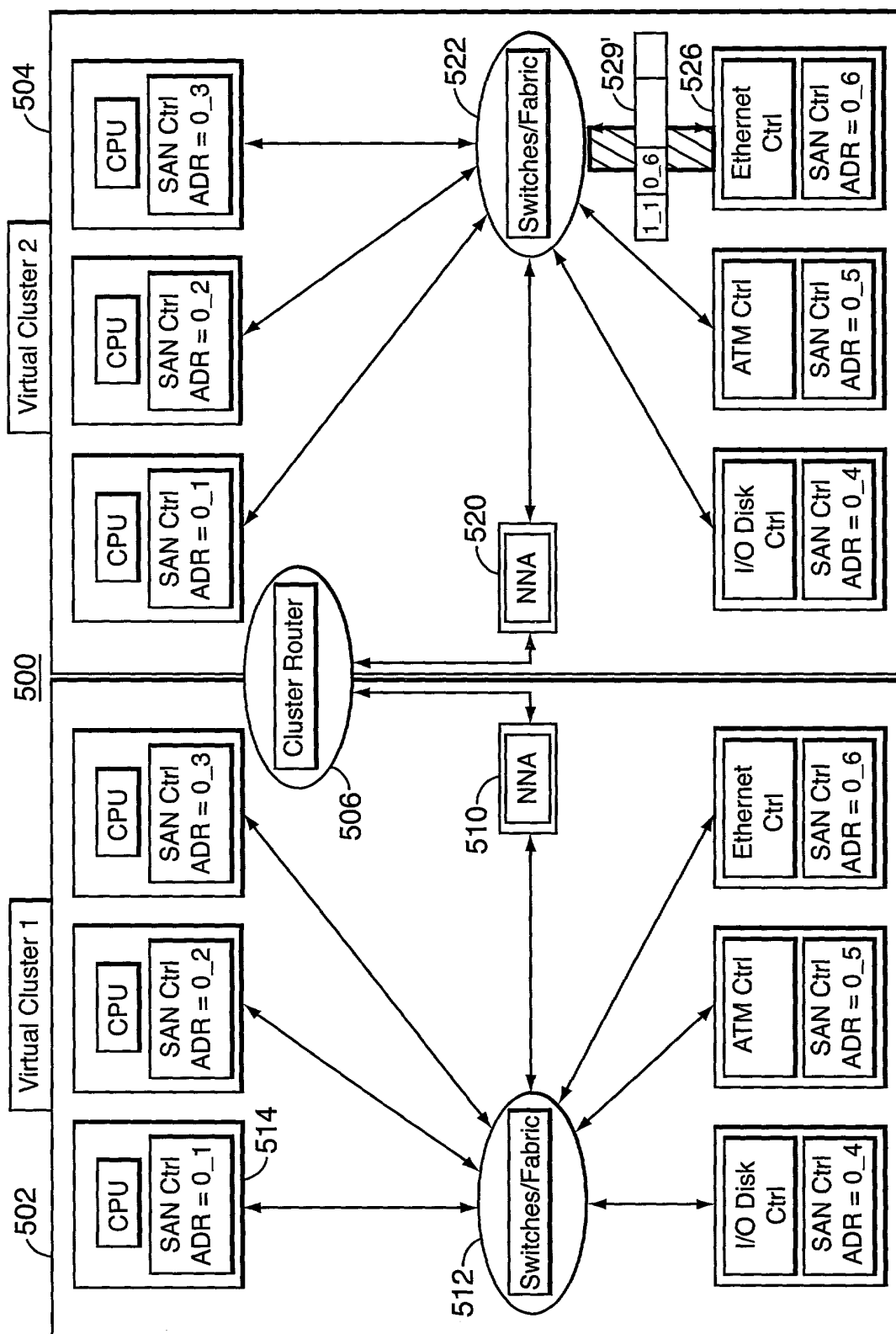


FIG. 12 Response Packet Flow - Step 1

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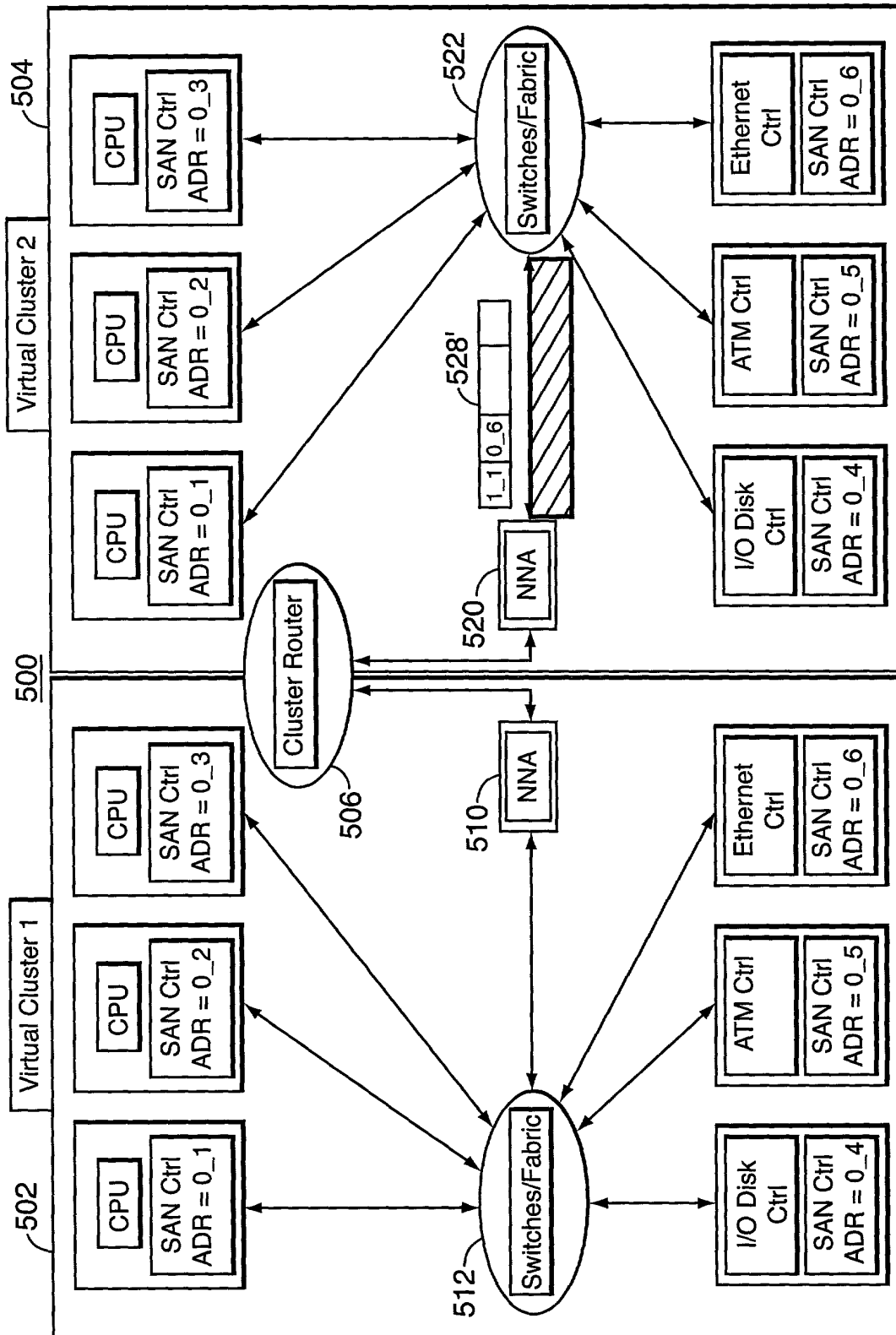


FIG. 13 Response Packet Flow - Step 2

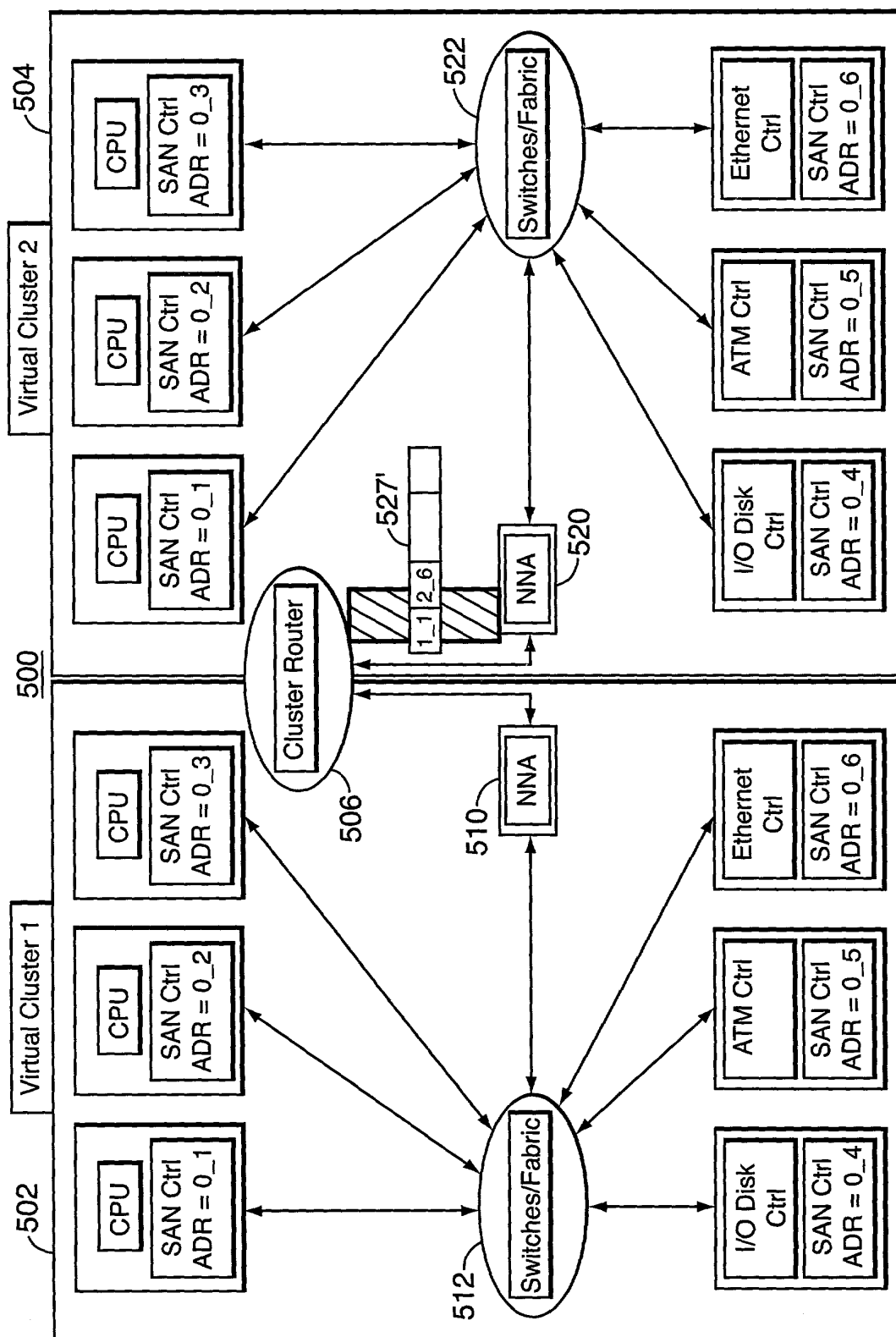
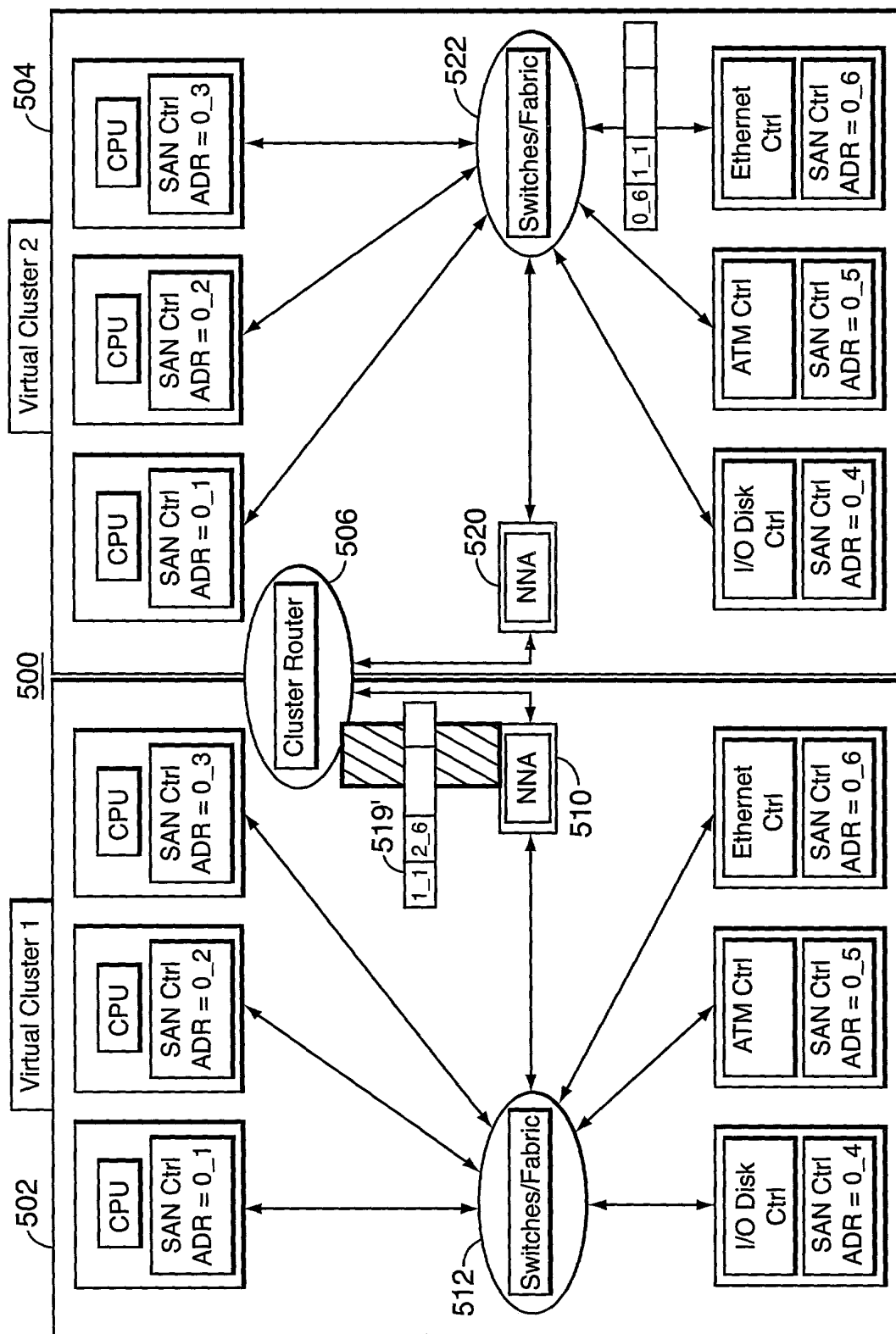


FIG. 14 Response Packet Flow - Step 3



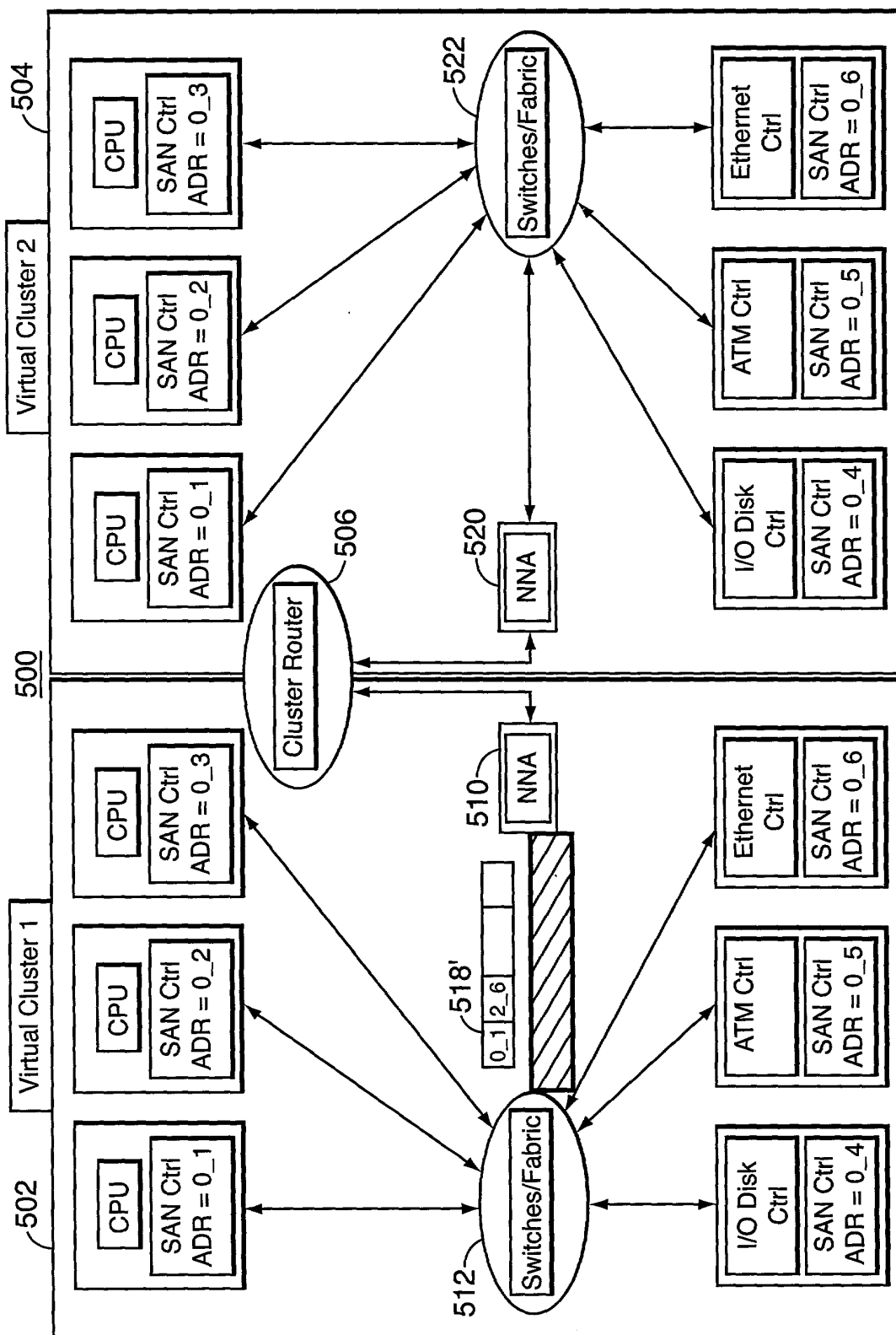


FIG. 16 Response Packet Flow - Step 5

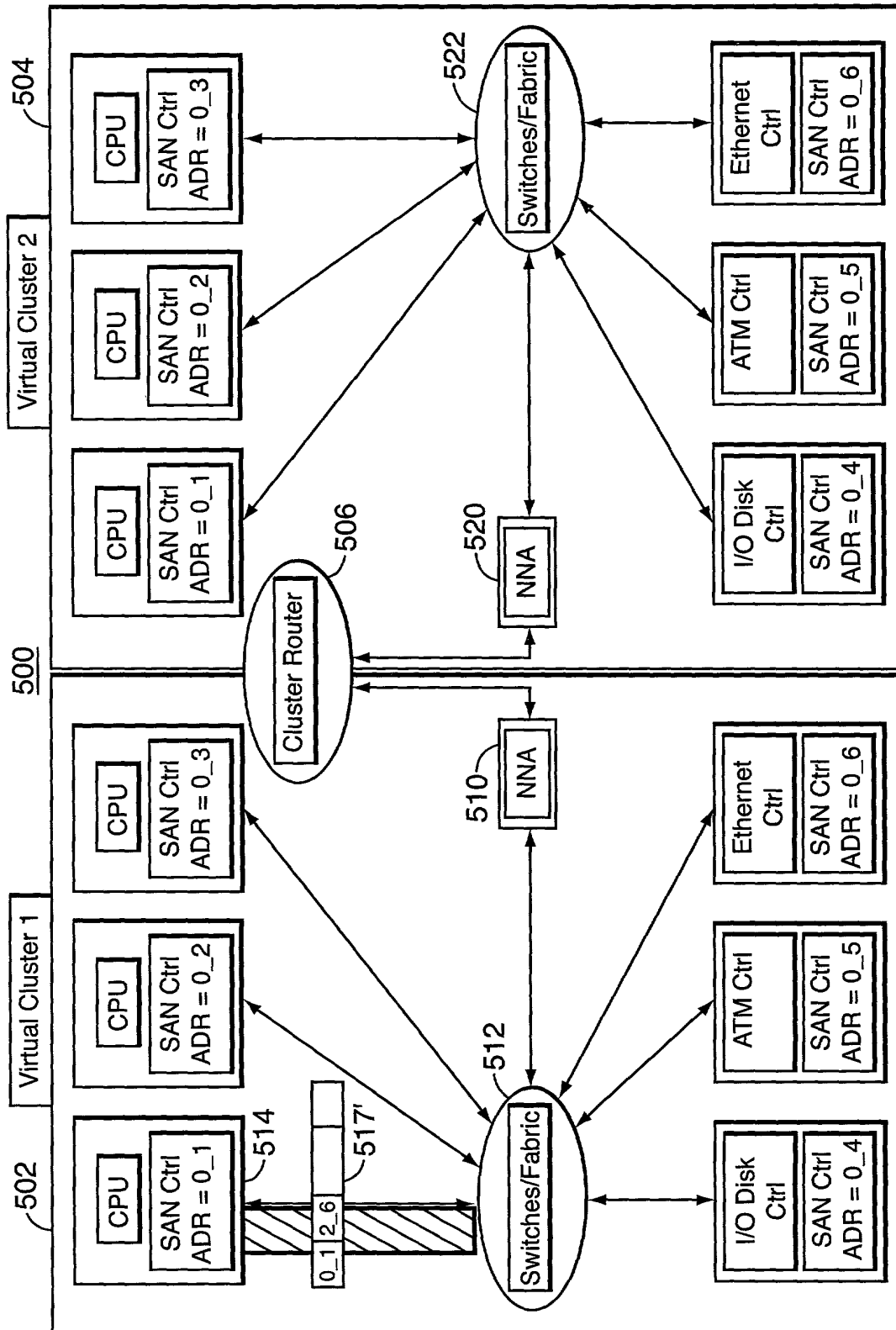


FIG. 17 Response Packet Flow - Step 6